

WEST Search History

DATE: Saturday, October 18, 2003

Set Name Query

side by side

DB=PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR

		<u>Hit Count</u>	<u>Set Name</u>
L41	l17 and l15	0	L41
L40	l25 and l10	0	L40
L39	L22 and l10	0	L39

DB=USPT; PLUR=YES; OP=OR

		<u>Hit Count</u>	<u>Set Name</u>
L38	5012514.pn.	1	L38
L37	5377269.pn.	1	L37
L36	5537544.pn.	1	L36
L35	5586301.pn.	1	L35
L34	5596718.pn.	1	L34
L33	5421006.pn.	1	L33
L32	5537540.pn.	1	L32
L31	5544344.pn.	1	L31
L30	5623673.pn.	1	L30
L29	5671422.pn.	1	L29
L28	5784625.pn.	1	L28
L27	5832299.pn.	1	L27
L26	l10 and L25	4	L26
L25	secure adj3 execution	123	L25
L24	5898843.pn.	1	L24
L23	5956743.pn.	1	L23
L22	5630147.pn.	1	L22
L21	5963738.pn.	1	L21
L20	6009520.pn.	1	L20
L19	6009524.pn.	1	L19
L18	l14 and L17	15	L18
L17	ACPI or (Advanced adj configuration adj2 power adj interface)	276	L17
L16	l14 and L15	0	L16
L15	secure adj execution adj mode\$1	1	L15
L14	l12 and L13	153	L14
L13	l10 and l3	291	L13
L12	l7 or L11	7137	L12
L11	((710/260 710/261 710/262 710/263 710/264 710/265 710/266 710/267 710/268 710/269)!.CCLS.)	1318	L11
L10	smi or L9	1215	L10

INVENTOR
SEARCH
DONE
10-18-03

SPL ✓

L9	system adj managme	adj interrupt	0	L9
L8	l3 and L7		131	L8
L7	l4 or l5 or l6		5885	L7
L6	((713/200 713/201 713/202 713/300 713/310 713/320 713/321 713/322 713/323 713/324 713/330 713/340)!.CCLS.)		4380	L6
L5	((713/100)!.CCLS.)		580	L5
L4	((713/1 713/2)!.CCLS.)		1374	L4
L3	l2 or l1		399	L3
L2	smm and l1		307	L2
L1	system adj management adj mode\$1		399	L1

END OF SEARCH HISTORY

WEST

Search Results - Record(s) 1 through 4 of 4 returned.

1. Document ID: US 5987604 A

L26: Entry 1 of 4

File: USPT

Nov 16, 1999

US-PAT-NO: 5987604

DOCUMENT-IDENTIFIER: US 5987604 A

TITLE: Method and apparatus for providing execution of system management mode services in virtual mode

CCLS: 713/1, 713/100

2. Document ID: US 5944821 A

L26: Entry 2 of 4

File: USPT

Aug 31, 1999

US-PAT-NO: 5944821

DOCUMENT-IDENTIFIER: US 5944821 A

TITLE: Secure software registration and integrity assessment in a computer system

CCLS: 713/200, 713/202

3. Document ID: US 5850559 A

L26: Entry 3 of 4

File: USPT

Dec 15, 1998

US-PAT-NO: 5850559

DOCUMENT-IDENTIFIER: US 5850559 A

TITLE: Method and apparatus for secure execution of software prior to a computer system being powered down or entering a low energy consumption mode

CCLS: 713/320, 714/38

4. Document ID: US 5748888 A

L26: Entry 4 of 4

File: USPT

May 5, 1998

US-PAT-NO: 5748888

DOCUMENT-IDENTIFIER: US 5748888 A

TITLE: Method and apparatus for providing secure and private keyboard communications in computer systems

CCLS: 713/200

[Full](#) | [Title](#) | [Citation](#) | [Front](#) | [Review](#) | [Classification](#) | [Date](#) | [Reference](#) | [Sequences](#) | [Attachments](#)[KOMC](#) | [Draw Desc](#) | [Image](#)[Generate Collection](#)[Print](#)

Terms	Documents
110 and L25	4

Display Format: [TI,CC](#) [Change Format](#)[Previous Page](#) [Next Page](#)

WEST

L26: Entry 3 of 4

File: USPT

Dec 15, 1998

US-PAT-NO: 5850559
 DOCUMENT-IDENTIFIER: US 5850559 A

TITLE: Method and apparatus for secure execution of software prior to a computer system being powered down or entering a low energy consumption mode

DATE-ISSUED: December 15, 1998

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Angelo; Michael F.	Houston	TX		
Miller; Craig A.	Cedar Park	TX		

ASSIGNEE-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY	TYPE CODE
Compaq Computer Corporation	Houston	TX			02

APPL-NO: 08/ 693458 [PALM]

DATE FILED: August 7, 1996

INT-CL: [06] G06 F 11/00, G06 F 1/00

US-CL-ISSUED: 395/750.03; 395/183.14

US-CL-CURRENT: 713/320; 714/38

FIELD-OF-SEARCH: 395/750.01-750.06, 395/183.14

PRIOR-ART-DISCLOSED:

U.S. PATENT DOCUMENTS

PAT-NO	ISSUE-DATE	PATENTEE-NAME	US-CL
<input type="checkbox"/> <u>5421006</u>	May 1995	Jablon et al.	395/575
<input type="checkbox"/> <u>5537540</u>	July 1996	Miller et al.	395/183.14

OTHER PUBLICATIONS

Microsoft Visual C++, Reference vol. 1, Class Library Reference, Version 1.0. 1993, p. 997.
 Intel486.TM. SL Microprocessor SuperSet System Design Guide, Nov. 1992, pp. 12-14 through 12-37.

Intel486.TM. SL Microprocessor SuperSet Programmer's Reference Manual, Nov. 1992, pp. 6-28 through 6-53.

ART-UNIT: 271

PRIMARY-EXAMINER: An; Meng-Ai T.

ASSISTANT-EXAMINER: Pancholi; Jigar

ATTY-AGENT-FIRM: Pravel, Hewitt & Kimball

ABSTRACT:

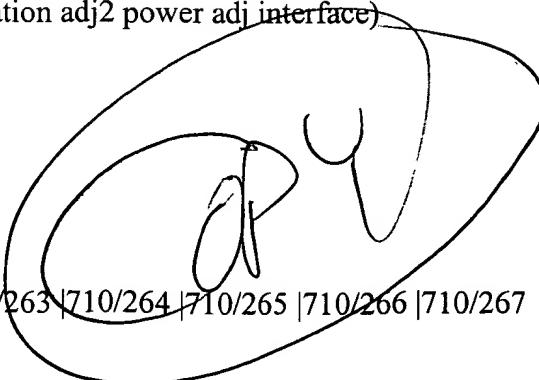
A computer system that automatically and securely executes registered programs immediately prior to a transition to a reduced energy consumption state. A registrar table specifying registered programs and a secure modification detection value for each registered program are maintained in system management mode memory or other secure memory space in the computer system. A system management interrupt is generated following a request to remove power from the computer system or the occurrence of an event that triggers an energy saving mode. The system management interrupt handler routine then generates a current modification detection value for each registered program. The current modification detection values are compared with the secure modification detection values. Execution of a registered program is permitted if the values match. After all registered programs have been executed, the computer system automatically powers down or enters an energy saving mode. The computer system thereby allows secure and convenient execution of programs or commands that would typically interfere with normal computer use.

23 Claims, 7 Drawing figures

WEST Search History

DATE: Saturday, October 18, 2003

<u>Set Name</u>	<u>Query</u>	<u>Hit Count</u>	<u>Set Name</u>
side by side		result set	
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